

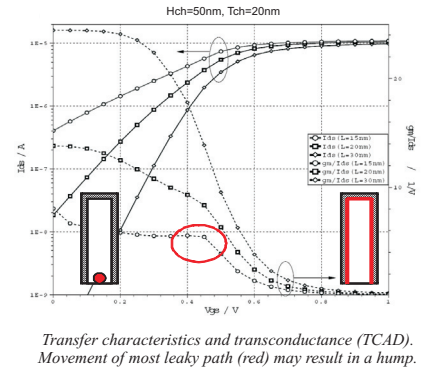
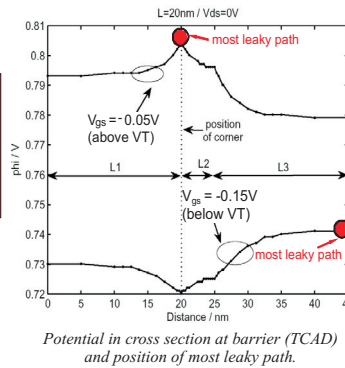
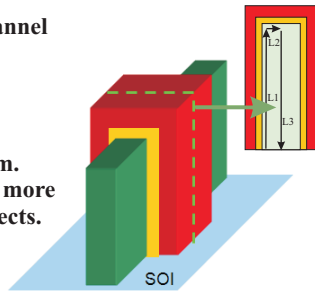


# Closed-form Current Equation for Short-Channel Triple-Gate FETs

Alexander Kloes,<sup>1</sup> Michaela Weidemann,<sup>1,2</sup> Mike Schwarz<sup>1,2</sup>  
<sup>1</sup>University of Applied Sciences Giessen-Friedberg, Germany  
<sup>2</sup>Universitat Rovira i Virgili, Tarragona, Spain

## Analysis

In multi-gate FETs the position of the most leaky path within the channel cross section is bias dependent:  
 - below  $V_T$ : at bottom center,  
 - above  $V_T$ : at silicon surface.  
 If the channel is tall the top gate loses control on the channel bottom. Hence the subthreshold slope gets more degraded due to short-channel effects. This can result in a hump in the transconductance.



## Modeling Approach

Our approach is based on a current equation from [1] which originally results from a 1D analysis of long-channel devices. We separately calculate the mobile charge for below ( $Q_{is,sth}$ ,  $Q_{id,sth}$ ) and above ( $Q_{is,str}$ ,  $Q_{id,str}$ ) threshold.

$$I_d = \mu W \left[ \frac{2V_{th}}{L - x_{min}} (Q_{id,sth} - Q_{is,sth}) - \frac{(Q_{id,str}^2 - Q_{is,str}^2)}{4LC'_{ox}} \right]$$

Below threshold the channel length  $L$  is shortened by the distance  $x_{min}$  of the potential barrier from the source end of the channel.

The mobile charge below threshold in the bottom center of the channel is calculated from a 3D analytical model for the potential profile  $\phi(x,y,z)$  at the barrier [2]:

$$Q_{is,sth} = \int \int q_i dy dz = \int \int \frac{n_i^2}{N_B} \exp\left(\frac{\phi(x_m, y, z)}{V_{th}}\right) dy dz$$

$$Q_{id,sth} = Q_{is,sth} \exp(-V_{ds}/V_{th})$$

Channel charge above threshold results from a standard bulk MOS charge-sheet approximation:

$$Q_{is,str} = 2C'_{ox}(V_{gy} - V_{T,s})$$

$$Q_{id,str} = 2C'_{ox}(V_{gy} - V_{T,s} - V_{ds})$$

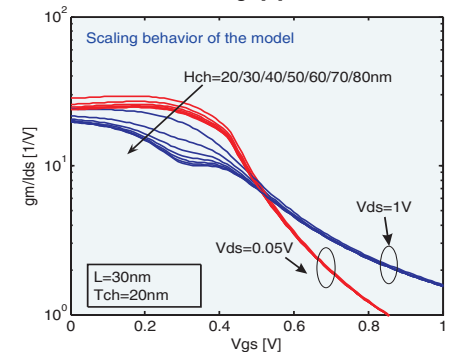
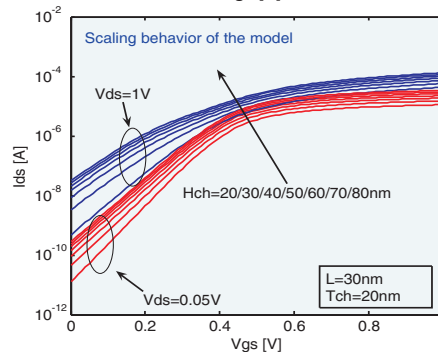
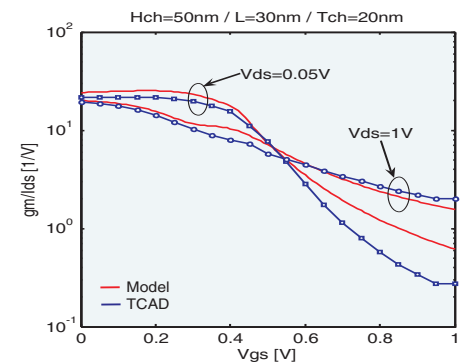
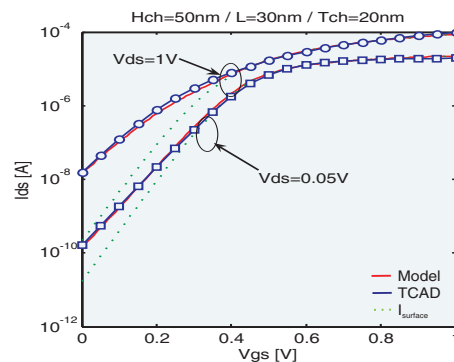
Voltage  $V_{gy}$  smoothly limits  $V_{gs} > V_T$  obtaining the subthreshold slope of the surface channel current [3]. Slope and  $V_T$  at the silicon-to-oxide interface are calculated from the 3D analytical model [2].

## Results

Plots show our model (red) compared to TCAD Sentaurus (blue). The hump in the transconductance is correctly predicted. The result of the surface current model only is shown in green dotted lines. It is obvious that the surface current (green) dominates the total current (red) above  $V_T$ , whereas below threshold the slope degradation must result from the current in the channel center.

An increase of the channel height reduces the influence of the top gate. Therefore short channel effects as DIBL and slope degradation take place. The 3D closed-form analytical current equation allows a structure oriented analysis how the aspect ratio  $H_{ch}/T_{ch}$  of the channel has an impact on slope,  $V_T$  and the transconductance.

The model does not introduce any fitting parameters for the 3D analysis. The only parameters are for mobility, saturation voltage and inversion potential at threshold.



### References:

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