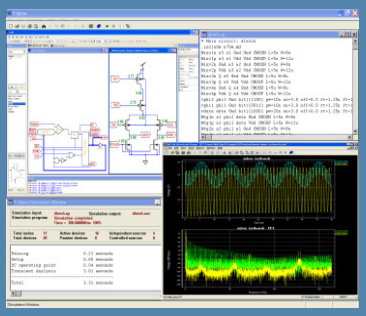
Speeding Concept to Silicon

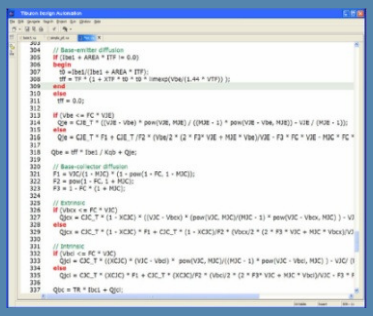
T-Spice™ puts you in control of simulation jobs with an easy-to-use graphical interface and a faster, more intuitive design environment. With the available Verilog-A option, designers can easily and quickly write their own custom behavioral models for early system level simulations.

T-Spice™ delivers highly accurate results by supporting the latest foundry, transistor, and behavioral models, with its new multithreading capability for faster runtimes, you will speed your design from concept to silicon faster than ever.

T-Spice™

CIRCUIT SIMULATION

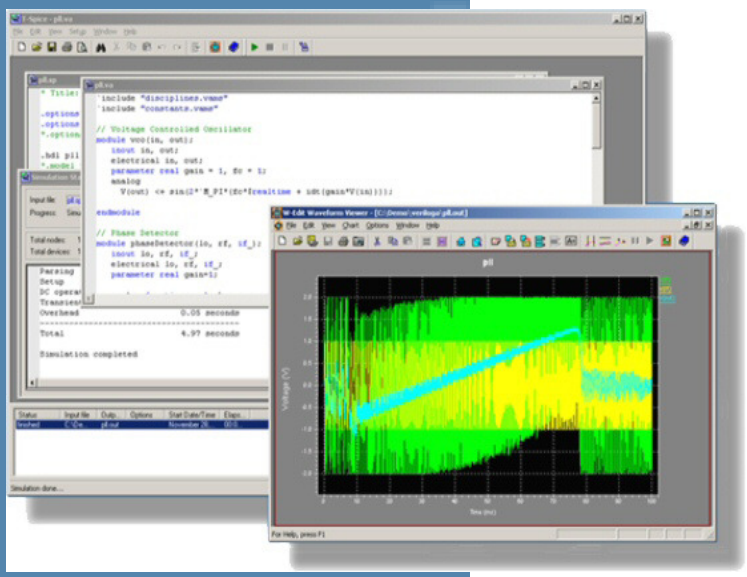




- Verilog-A support enables model developers to specify compact models in a high level behavioural language, and designers to prove system level designs before doing full device level design.

- Support for PSP, BSIM3.3, BSIM4.5, BSIM SOI 4.0, EKV 2.6, MOS 9, 11, 20, 30, 31, 40, RPI a-Si & Poly-Si TFT, VBIC, Modella, and MEXTRAM models.

For more information:
www.eda-solutions.com



from software to silicon

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