

# Large signal models validation at circuit and device level by defining different metrics

Monica F. Barciela, Dominique Schreurs\* and Giorgio Vannini\*\*

Universidad de Vigo, Dept. of Signal Theory & Comm., 36310 Vigo, Spain

\*K.U.Leuven, Div. ESAT-TELEMIC, B-3001 Leuven, Belgium

\*\*University of Ferrara, Dept. of Engineering, 44100 Ferrara, Italy



UNIVERSIDADE DE VIGO



Contributors

M.Pirazzini\*\*, A. Alabadelah\*, E. Sánchez and G. Fernández

## I. INTRODUCTION

### Large-signal (LS) empirical models

- ✓Used at circuit & device levels, useful in MMICs desing. Many models proposed: analytical, table based...
- ✓Difficult to determine the best model for a circuit or device, and a given application. Different behaviour has to be predicted and, as a consequence, different figures of merit can be of relevance depending on the applications.

### This Work:

- ✓Metrics to validate/compare LS models investigated.
- ✓Metrics validity studied comparing – static & dynamic-predicted and measured performance in the case of a LDMOS PA and different transistors (HBT, NMOS).
- ✓Joint effort within the NoE TARGET.

## II. METRICS

### DC

$$M1_{DC} = \frac{1}{N} \sum_{i=1}^N |I_{C_{measured}} - I_{C_{simulated}}|$$

$$M2_{DC} = \frac{1}{N} \sqrt{\sum_{i=1}^N (I_{C_{measured}} - I_{C_{simulated}})^2}$$

$$M3_{DC} = \sqrt{\frac{\sum_{i=1}^N (I_{C_{measured}} - I_{C_{simulated}})^2}{N}}$$

$$M4_{DC} = \frac{\sum_{i=1}^N |I_{C_{measured}} - I_{C_{simulated}}|}{\sum_{i=1}^N I_{C_{measured}}}$$

N: no. bias points

### Small-Signal

$$M1_{SP} = \frac{\sum_{i=1}^M \sum_{j=1}^N |S_{measured} - S_{simulated}|}{\sum_{i=1}^M \sum_{j=1}^N |S_{measured}|}$$

$$M2_{SP} = \sqrt{\frac{\sum_{i=1}^M \sum_{j=1}^N (G_{measured} - G_{simulated})^2}{\sum_{i=1}^M \sum_{j=1}^N G_{measured}^2}}$$

$$M3_{SP} = \sqrt{\frac{\sum_{i=1}^M \sum_{j=1}^N (K_{measured} - K_{simulated})^2}{\sum_{i=1}^M \sum_{j=1}^N K_{measured}^2}}$$

N: no. bias points, M: no. frequency points

### Large-Signal

$$M1_{LS} = \frac{\sum_{i=1}^H \sum_{j=1}^N |P_{out}^{measured} - P_{out}^{simulated}|}{\sum_{i=1}^H \sum_{j=1}^N |P_{out}^{measured}|}$$

$$M1_{LS,W} = \frac{\sum_{i=1}^H \sum_{j=1}^N |P_{out}^{measured} - P_{out}^{simulated}|}{\sum_{i=1}^H \sum_{j=1}^N |P_{out}^{measured}|}$$

$$M2_{LS} = \sqrt{\frac{\sum_{i=1}^H \sum_{j=1}^N (I_{out}^{measured} - I_{out}^{simulated})^2}{\sum_{i=1}^H \sum_{j=1}^N I_{out}^{measured}^2}}$$

$$M3_{LS} = \sqrt{\frac{\sum_{i=1}^H \sum_{j=1}^N (Gain^{measured} - Gain^{simulated})^2}{\sum_{i=1}^H \sum_{j=1}^N Gain^{measured}^2}}$$

$$M4_{LS} = \frac{\sum_{i=1}^H \sum_{j=1}^N |PAE^{measured} - PAE^{simulated}|}{\sum_{i=1}^H \sum_{j=1}^N |PAE^{measured}|}$$

$$M4_{LS,W} = \frac{\sum_{i=1}^H \sum_{j=1}^N |PAE^{measured} - PAE^{simulated}|}{\sum_{i=1}^H \sum_{j=1}^N |PAE^{measured}|}$$

H: no. harmonics, N: no. bias points, M: no. input power levels

Most convenient the use of normalized metrics

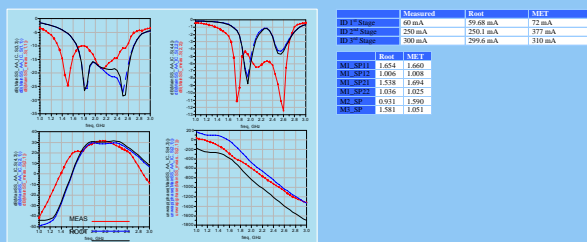
## III. LS MODEL VALIDATION & METRICS EVAL

A. Circuit Level Evaluation: RF LDMOS PA for W-CDMA base station applications (MW41C2230, Motorola & Freescale Semic.). LS models: MET & ROOT

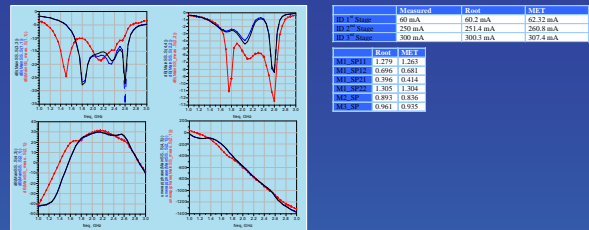
Model	DC Bias	RF Power	DC Power	Measurements by Freescale
MEAS	200 mA	200 mW	200 mW	200 mW
ROOT	200 mA	200 mW	200 mW	200 mW
MET	200 mA	200 mW	200 mW	200 mW

### DC & S-parameters

Non-accurate textfixture model



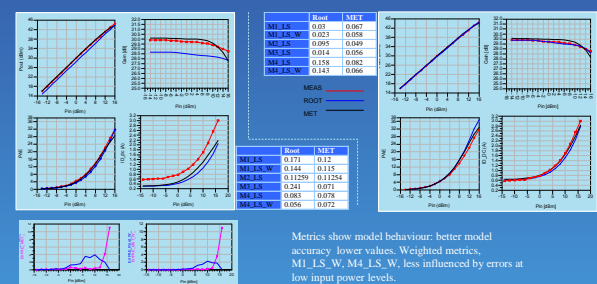
### Accurate textfixture model



### Single-tone LS excitation

Non-accurate

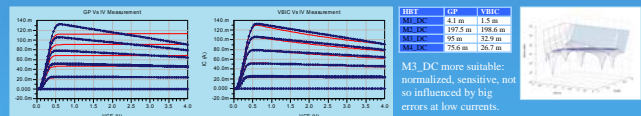
Accurate textfixture model



Metrics show model behaviour; better model accuracy lower values. Weighted metrics, M1\_LS, W, M4\_LS, W, less influenced by errors at low input power levels.

## B. Devic Level Evaluation:

A. HBT: LS models: VBIC & Gummel-Poon

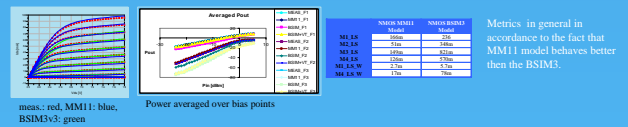


### S-parameters

Model	DC Bias	RF Power	DC Power
MEAS	200 mA	200 mW	200 mW
ROOT	200 mA	200 mW	200 mW
MET	200 mA	200 mW	200 mW

All AC metrics behave accordingly to model accuracy for each meas. set. But difficult to determine the best model. AC metrics open question.

A. NMOS: LS models: BSIM3v3 & MM11



Metrics in general in accordance to the fact that MM11 model behaves better than the BSIM3.

## IV. Conclusions

We have validated and compared LS models for devices and circuits. We studied different metrics in order to compare the accuracy of those models. Normalized metrics are more appropriate for such comparison. In the case of DC validation, metrics M3\_DC & M4\_DC are the more suitable. In the case of small-signal AC metrics, still more work is required. Large signal metrics show consistency with model behaviour and they seem to point out in the right direction.

## V. Acknowledgments

This work was supported by the Spanish Ministry of Science and Education TIC 2002-03167 and EU IST-1-507893-NOE.

## VI. References

- [1] J. Sotomayor, M. Goto, C. Wood and M.C. de Boer, "Considerations for Improving the Accuracy of Large-Signal GaAs MESFET Models to Predict Power Amplifier Circuit Performance", IEEE Journal of Solid-State Circuits, March 1994.
- [2] M. Miller, M. Gollo, B. Beckwith, E. Arnold, D. Halchin, S. Agno and S. Dorn, "Choosing an Optimum Large-Signal Model for GaAs MESFETs", IEEE MTT-S Digest, Vol. 3, pp. 1275-1282, May 8-10 1990.
- [3] C. Baylis, L. Dunleavy and A.D. Snider, "The Normalized Difference Unit as a Metric For Comparing IV Curves" IEEE ARFTG Microwave Measurements Conference, pp. 93-100, December 2004.