

# Physics-based compact model for the Surrounding-Gate MOSFET

D. Jiménez<sup>1\*</sup>, B. Iñiguez<sup>2</sup>, J. Suñé<sup>1</sup>, L. F. Marsal<sup>2</sup>, J. Pallarès<sup>2</sup>, J. Roig<sup>3</sup>, and D. Flores<sup>3</sup>

<sup>1</sup>Departament d'Enginyeria Electrònica; ETSE; Universitat Autònoma de Barcelona; Barcelona (Spain)

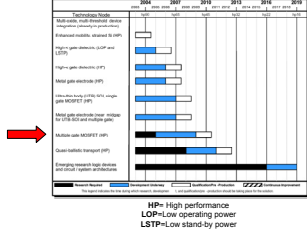
<sup>2</sup>Departament d'Enginyeria Electrònica, Elèctrica i Automàtica; ETSE; Universitat Rovira i Virgili; Tarragona (Spain)

<sup>3</sup>Instituto de Microelectrónica de Barcelona (IMB-CNM); Centro Superior de Investigaciones Científicas; Barcelona (Spain)

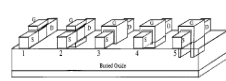
\* Corresponding author: David.Jimenez@uab.es

**Abstract:** We present a continuous analytic current-voltage model for cylindrical undoped (lightly doped) surrounding-gate (SGT) MOSFETs. It is based on the exact solution of the Poisson's equation, and the current continuity equation without the charge-sheet approximation, allowing the inversion charge distribution in the silicon film to be adequately described. It is valid for all the operation regions (linear, saturation, subthreshold) and traces the transition between them without fitting-parameters, being ideal for the kernel of SGT-MOSFETs compact models. We have demonstrated that the current-voltage characteristics obtained by this model agree with three-dimensional numerical simulations for all ranges of gate and drain voltages.

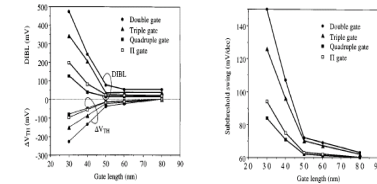
### Extending the scalability of CMOS technology



### Multiple-gate MOSFETs

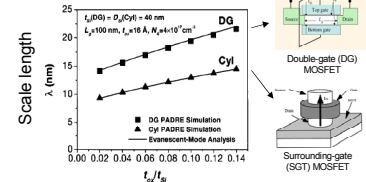


Gate configurations for SOI devices



J. P. Colinge et al., EDL 2003

### Multiple-gate MOSFETs



### The Surrounding-gate (SGT) MOSFET



Excellent device for continuing scalability of the CMOS technology

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|---|---|
| <b>Advantages</b> <ul style="list-style-type: none"> <li>Maximum control of Short-Channel Effects = best scaling potential</li> <li>Higher channel mobility than the bulk-MOSFET</li> </ul> | <b>Problems to be solved</b> <ul style="list-style-type: none"> <li>Difficult to fabricate</li> <li>Threshold voltage control difficult by conventional means: gate electrode workfunction engineering</li> </ul> |
|---|---|

### Physics-based model for the SGT-MOSFET

1D Poisson's equation for undoped/lightly doped channels

$$\frac{d^2\psi}{dr^2} + \frac{1}{r} \frac{d\psi}{dr} = \frac{q n_{in}}{\epsilon_{si} \epsilon_0} \quad \text{Mobile charge}$$

Boundary conditions	
Symmetry	$\frac{d\psi}{dr}(r=0) = 0$
Surface potential	$\psi(r=R) = \psi_s$

Gradual channel approximation:  $V = V(y)$

Potential	$\psi(r) = V + \frac{kT}{q} \ln \left( \frac{-8B}{\eta \beta \left( 1 + B^2 \right)} \right) \quad (1)$
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B is a constant related with the surface potential

From Gauss's law the following relation must hold:

$$C_{ox}(V_{gs} - \Delta\phi - \psi_s) = \epsilon_{si} \frac{d\psi}{dr} \Big|_{r=R} = Q \quad (2)$$

Substituting the analytical solution for the potential (1) into (2) yields:

$$\frac{q(V_{gs} - \Delta\phi - V)}{kT} = \ln \left( \frac{8}{\eta \beta} \right) = \ln(1 - \beta) - \ln \beta^2 + \eta \left( \frac{1 - \beta}{\beta} \right) \quad (3)$$

where  $\beta = 1 + BR^2$  is a constant (of  $r$ ) to be determined from (3), and  $\eta = 4\epsilon_{ox}/C_{ox}R$  is a structural parameter.

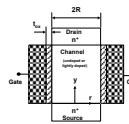
For a given  $V_{gs}$ ,  $\beta$  can be solved from (3) as a function of  $V$ .

$V(y)$  and  $I(y)$  is determined by the current continuity equation, which requires the drift-diffusion current  $I_{DS} = \mu(2\pi R)QV/dy = \text{constant}$ , independent of  $V$  or  $y$ .

Integrating  $I_{DS} dy$  from the source to the drain, the current can be written as:

$$I_{DS} = \mu \frac{2\pi R^2}{L} \int_0^{V_{DS}} Q(V) dV = \mu \frac{2\pi R^2}{L} \int_0^{V_{DS}} Q(\beta) \frac{dV}{d\beta} d\beta \quad (4)$$

where  $\beta_S, \beta_D$  are solutions to (3) corresponding to  $V=0$  and  $V=V_{DS}$  respectively.



By using (1) and replacing it into  $Q(\beta) = \epsilon_{si} d\psi/dr$ :  $Q(\beta) = (2\epsilon_{si}/kT)q(1/\beta R)(1-\beta)$ . Note that  $dV/d\beta$  can also be expressed as a function of  $\beta$  by differentiating (3).

Substituting these factors in (4), integration can be performed analytically to yield:

$$I_{DS} = \mu \frac{4\pi\epsilon_{si}}{L} \left( \frac{2kT}{q} \right)^2 \left[ \frac{\eta}{4\beta^2} + \frac{1-\eta/2}{\beta} + \frac{1}{2} \ln \beta \right]_{\beta_S}^{\beta_D} \quad (5)$$

To compute  $I_{DS}$  we define the following two functions representing the RHS of (3) and (5):

$$f(\beta) = \ln(1 - \beta) - \ln \beta^2 + \eta \left( \frac{1 - \beta}{\beta} \right) \quad (6)$$

Note that the range of  $\beta$  is  $0 < \beta < 1$

$$g(\beta) = \frac{\eta}{4\beta^2} + \frac{1 - \eta/2}{\beta} + \frac{1}{2} \ln \beta \quad (7)$$

For given  $V_{GS}$  and  $V_{DS}$ ,  $\beta_S$  and  $\beta_D$  are calculated from the conditions  $f(\beta_S) = q(kT)(V_{GS} - V_S)$  and  $f(\beta_D) = q(kT)(V_{GS} - V_D - V_{DS})$

where  $V_0 = \Delta\phi + \frac{kT}{q} \ln \left( \frac{8}{\eta \beta} \right) \quad (8)$

From (7), the drain current  $I_{DS} = \mu g(\beta_S) g(\beta_D)$  can be easily computed

### Operation regions

1) **Linear region above threshold:**  $f(\beta_S), f(\beta_D) > 1$ , so  $\beta_S, \beta_D > 0$ .

$$I_{DS} = 2\mu C_{ox} \frac{\pi R}{L} \left( V_{GS} - V_0 - \frac{V_{DS}}{2} \right) V_{DS}$$

where  $V_0 = V_{GS} - \eta(kT/q)$  is the threshold voltage.  $V_0$  shifts more than 120 mV per decade of variation on  $R$  (at room temperature), being this shift more important for higher  $t_{ox}/R$  ratios.

2) **Saturation region:**  $f(\beta_S) > 1$  and  $f(\beta_D) < -1$ , so  $\beta_S > 0$  and  $\beta_D < -1$ .

$$I_{DS} = \mu C_{ox} \frac{\pi R}{L} \left\{ \left( V_{GS} - V_0 \right)^2 - \left( \frac{kT}{q} \right)^2 \left[ \frac{\eta^2}{\left( 1 - e^{-\frac{q(V_{GS} - V_0)}{kT}} \right)^2} + \frac{4\eta(1 - \eta/2)}{\left( 1 - e^{-\frac{q(V_{GS} - V_0)}{kT}} \right)} \right] \right\}$$

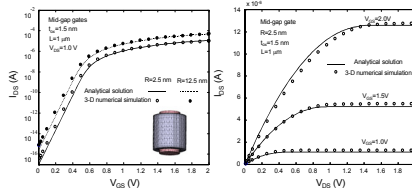
The saturation current depends on  $(V_{GS} - V_0)^2$ , as expected for a MOSFET

3) **Subthreshold region:**  $f(\beta_S), f(\beta_D) < -1$ , so  $\beta_S, \beta_D < -1$ .

$$I_{DS} = \mu \frac{\pi R^2}{L} n_i k T e^{\frac{q(V_{GS} - \Delta\phi)}{kT}} \left( 1 - \frac{qV_{DS}}{kT} \right)$$

The subthreshold current is proportional to the cross-sectional area of the SGT-MOSFET and independent of  $t_{ox}$ . This is a characteristic of the volume inversion phenomenon that cannot be captured by standard charge-sheet based models.

### Model validation



Transfer characteristics obtained from the analytical model compared with numerical simulations from DESSIS-ISE®.

Output characteristics obtained from the analytical model compared with numerical simulations from DESSIS-ISE®.

### Conclusions

We have presented a simple analytical I-V model suitable for compact modeling of undoped (lightly doped) SGT-MOSFETs. All the regions of operation and the transitions are correctly described by preserving the physics. In particular, the volume inversion, that cannot be captured by using the charge-sheet approximation, is well accounted for in this model.

The presented long-channel model is ideally suited for being the kernel of a SGT-MOSFET compact model. In order to complete the model, SCE, quantum effects, low and high field transport, noise, and more, should be added.

### References

- [1] J.-P. Colinge, "Multiple-gate SOI MOSFETs," *Solid-State Electron.*, vol. 48, no. 6, pp. 897-905, June 2004.
- [2] Y. Tsaur, X. Liang, W. Wang, and H. Lu, "A continuous, analytic drain-current model for DG-MOSFETs," *IEEE Electron Device Lett.*, vol. 25, no. 2, pp. 107-109, Feb. 2004.
- [3] H. C. Pao and C. T. Sah, "Effects of diffusion current on characteristics of metal-oxide (insulator)-semiconductor transistors," *Solid-State Electron.*, vol. 9, pp. 907-937, Oct. 1966.
- [4] Y. Tsaur and T. H. Ning, *Fundamentals of Modern VLSI Devices*. Cambridge, U.K.: Cambridge University Press, 1998.
- [5] D. Jiménez, J. J. Sñenz, B. Iñiguez, J. Suñé, L. F. Marsal, and J. Pallarès, "Modeling of nanoscale gate-all-around MOSFETs," *IEEE Electron Device Lett.*, vol. 25, pp. 314-316, May 2004.
- [6] Y. Chen and J. Luo, "A comparative study of double-gate and surrounding-gate MOSFETs in strong inversion and accumulation using an analytical model," in *Technical Proceedings of the International Conference on Modeling and Simulation of Microsystems*, 2001, pp. 546-549.
- [7] P. L. Chambré, "On the solution of the Poisson-Boltzmann equation with application to the theory of thermal explosions," *J. Chem. Phys.*, vol. 20, no. 11, pp. 1785-1797, Nov. 1952.