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Agenda

• Motivation
• Integrated Inductor
• Co-Planar Waveguide
• Conclusion
Motivation
• Commercial CMOS technology, silicon based, has evolved throughout the years to now become the best alternative for many wireless applications.

• It's a mature, well understood, and inexpensive technology.

• MOS Transistors have been built to operate at frequencies of hundreds of GHz.

• With these, complex ICs have been designed and manufactured, allowing for more on-chip functions than ever before.

• This trend will continue for many years, even though we are reaching the physical limits of integration.
CMOS Terahertz Receivers

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Abstract—Recent advances of devices and circuits have made CMOS (Complementary Metal Oxide Semiconductor) integrated circuits technology an alternative for realizing capable and affordable THz systems. Coherent detection up to 410 GHz and incoherent detection up to 10 THz as well as an almost fully integrated receiver working from 225-280 GHz have been demonstrated using CMOS. Despite the fact that \(f_{\text{max}}\) of NMOS transistors has peaked around 320 GHz, it should be possible to coherently detect signals at frequencies beyond 1 THz and with some straightforward modification of processes, to incoherently detect signals at 40 THz in CMOS.

Keywords—coherent; incoherent; receiver; detector; CMOS; THz; Sub-millimeter wave scaling with the technology nodes has made generation of a local oscillator (LO) signal with a necessary amplitude for mixing operation more challenging.

B. Interconnects

Metal interconnects of CMOS technologies have a significant impact to the terahertz performance of devices and circuits. Interconnect parasitics reduce \(f_{\text{max}}\) of NMOS transistors [11]. For instance, in 45-nm SOI (Silicon On Insulator), adding metal connections to the top metal layer for making interconnection to other passive and active devices lowers \(f_{\text{max}}\) from \(-400\) GHz to \(280\) GHz [11].
CMOS Platform for Terahertz

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Abstract—The Complementary Metal Oxide Semiconductor (CMOS) integrated circuits technology has emerged as a means for realization of capable and affordable systems that operate at 300 GHz and higher. This is bridging the Terahertz Gap and enabling everyday life applications utilizing this portion of the spectrum. Signal generation up to 1.3 THz, coherent detection up to 1.2 THz, and incoherent detection up to ~10 THz have been demonstrated using CMOS. Furthermore, a highly integrated rotational spectroscopy transceiver for electronic smelting operating up to near 300 GHz, a 30-Gbps 300-GHz QPSK transmitter for data communication with an output power of -6 dBm and an imaging array operating at 820 GHz have been demonstrated in CMOS. These along with the data in the literature suggest that the necessary terahertz electronics for everyday life applications can be affordably manufactured.

Keywords—CMOS, terahertz, devices, circuits, systems, varactors, Schottky diodes, everyday applications

has been demonstrated [9]. These limit the maximum frequency for linear amplification using transistors in CMOS to ~300 GHz or less [9]. Additionally, the decrease of supply voltage with the technology scaling is making generation of a sufficient power level in amplifiers and local oscillators (LO’s) needed for mixing operation more difficult.

Despite these limitations, it is possible to operate CMOS circuits above 300 GHz. Similar to what the terahertz community has done for many years using III-V devices and photonic crystals, the nonlinearity of components in CMOS can be utilized to accomplish this. In fact, Schottky diodes [9] with a cut-off frequency \((2\pi RC)^{-1}\) and MOS varactor diodes [9] with a dynamic cut off frequency \(f_{\text{cd}} = (2\pi R)^{-1} (1/C_{\text{min}}/C_{\text{max}})\) over 2 THz have been reported in CMOS (Fig. 1). With increasing cut-off frequencies, responsivity (output voltage/input power) of Schottky diode detectors and conversion efficiency of varactor frequency multipliers and mixers are improved at a given operating frequency. Increasing cut-off frequencies also

See also: “Opening Terahertz for Everyday Applications”, K.O. Kenneth et al., IEEE Communications Magazine, August 2019, pp. 70-76. DOI: 10.1109/MCOM.2019.1800909
Pushed to the Limit: A CMOS-based transceiver for beyond 5G applications at 300 GHz

Scientists at Tokyo Institute of Technology and NTT Corporation develop a novel CMOS-based transceiver for wireless communications at the 300 GHz band, enabling future beyond-5G applications. Their design addresses the challenges of operating CMOS technology at its practical limit and represents the first wideband CMOS phased-array system to operate at such elevated frequencies.

![Chip micrograph of 300 GHz-band phased-array transceiver implemented by 35 nm CMOS](image)

**Figure 1.** Chip micrograph of 300 GHz-band phased-array transceiver implemented by 35 nm CMOS.
Nowadays, it is hard to imagine daily life without connecting to cellular communications or wireless local area networks. People are not just accessing communications systems relying primarily on massive signal processing for orthogonal frequency division multiplexing and channel estimation/calibration, which has resulted in dramatically enhanced spectral efficiency and data rates.
• But CMOS ICs are not only made of transistors!
• To connect these, interconnect lines are necessary.
• To reach the external world, also.
• To implement filters, capacitors and inductors are needed.
• To convert voltages to currents, resistors are used.
• All these passive devices play an important role in circuit structure.
• In fact, in a complex IC, we might find hundreds of thousands of these.
• They all have an influence on the overall behavior of the circuit.
• Hence, they have to be studied, modeled, and characterized.
For the last three decades, we have dedicated our research efforts to the modeling, measurement and characterization of active and passive devices used for wireless communications, mostly CMOS.
But our work also involves HF effects on PCBs, antennas, and antenna arrays for communications and energy harvesting.
Here we present just two aspects of the work needed in this continuously expanding field of endeavor.
• Measurements were performed using a VNA.
• The system was previously calibrated using an external LRRM routine.
• All measurements were de-embedded following a three step method.
• The frequency span used for these studies went from 10 MHz to 60 GHz.
Quasi Static or NQS?

@60 GHz, $\lambda = 5,000 \ \mu m$

@300 GHz, $\lambda = 1,000 \ \mu m$
Integrated Inductor
• Inductors are probably the most important passive devices used in radio frequency ICs.
• An important figure of merit for inductors is the Quality Factor (Q).
• The value of Q strongly depends on the losses associated with eddy-currents on the ground path.
• To reduce these losses, ground shields are used underneath the inductors.
• These can be solid (SGS) or patterned (PGS).
• Shields can be built with metal, polysilicon, or low resistivity buried layers.
• A variety of inductors, from IMEC, were available for this study.
Micrograph of some of the fabricated inductors
Schematic showing SGS and PGS

• But practical inductors on ICs became common only in the late 1990s.

• Many models have been presented through the years, always changing as frequency of operation increases.

• Most are based on \( \pi \) and T equivalent circuit representations.
Traditional π Model

\[ Y_f = -Y_{12} \]
\[ Y_o = Y_{11} - Y_{12} \]
\[ Y_i = Y_{11} - Y_{12} \]
Traditional T Model
• For this work, we have adapted a traditional T model in order to represent the behavior of inductors up to 60 GHz: J. Valdés, R. Torres, R. Murphy, G. Álvarez, “Modeling Ground-Shielded Integrated Inductors Incorporating Frequency-Dependent Effects and Considering Multiple Resonances”, IEEE Transactions on Microwave Theory and Techniques, Vol. 67, No. 4, April 2019, pp. 1370-1378. DOI: 10.1109/TMTT.2019.2895579

• This model provides very good correlation with experimental data.
Proposed Model
• The values for all the components of the model were obtained from experimental data by defining linear relations in terms of frequency.

• A detailed description of the methodology is presented in the article in reference.

• For instance, if:

\[
\frac{\omega^2 L_{ind0}}{\Re(Z_{eddy})} \approx \frac{\tau}{k} \omega^2 + \frac{1}{k \tau}
\]

• The slope of a linear regression is \( \tau/k \), whereas the intercept is \( (1/k \tau) \).
• An important factor taken into consideration for the model is the frequency dependence of resistance and inductance:

\[ R_{ind} = R_{ind0} + k_s \sqrt{f} \]

\[ L_{ind} = L_\infty + \frac{k_s}{2\pi \sqrt{f}} \]

• Thus, the skin effect is satisfactorily taken into account.
• Proximity effects are also considered.
Results

- This graph shows some of the comparisons of the proposed model to experimental data.
- Also shown are the comparisons with the traditional $\pi$ and T models.
- The model derived from this work shows very good correlation up to 60 GHz.
• Inductors built on a PGS show a narrower bandwidth, attributed to the reduction of eddy-current losses.
• This makes them appropriate for filter construction.
• Otherwise, there is not a clear distinction between SGS and PGS.
• The depth of the ground shield (h) influences the number of resonances; the larger h is, the higher the frequency for the additional resonances.
• The model is physically based, and does not include artificial components to attain a good correlation.
• It is probably the first one that represents different types of inductors up to 60 GHz.
Co-Planar Waveguide
• All ICs require interconnects from device to device, and device to platform (pads).
• These are fundamental parts of the circuit, and thus influence its response to varying degrees.
• In order to have trustworthy simulations, which in turn lead to reliable fabrication, interconnects also have to be characterized and modeled, especially so in the HF regime.
• This work aims at improving the models for Co-Planar Waveguides (CPW), a very common form of interconnect in current technologies.
• The details of the work can be found in: J. Valdés, R. Murphy, R. Torres, “Determination of the Contribution of the Ground-Shield Losses to the Microwave Performance of On-Chip Coplanar Waveguides”, IEEE Transactions on Microwave Theory and Techniques, Vol. 69, No. 3, March 2021, pp. 1594-1601. DOI: 10.1109/TMTT.2021.3053548

• Extensive characterization of CPWs has been carried out by many researchers throughout the years.

• Here, we only address two issues, both parasitic: a) Transverse resistance introduced by the PGS. b) Coupling between input and output ports accentuated in relatively short lines.
• The structures available for this study were a “short” line (250 \(\mu m\)) and a “long” line (2,000 \(\mu m\)), in a 50 \(\mu m\) pitch GSG configuration.
• The structures present a different gap-width between the signal and ground lines; 2 \(\mu m\), 5 \(\mu m\), and 10 \(\mu m\).
• They were fabricated at IMEC.
• Measurements were performed using a previously calibrated VNA, using a LRRM routine.
\[ l_s = 250 \, \mu m \]

\[ l_L = 2000 \, \mu m \]
• Treating CPWs as transmission lines is common practice in RF circuit characterization.
• This way, there is no confusion on whether the measurement should be QS or NQS, as the method applies equally in both cases.
• Thus, the complex propagation “constant” has to be determined from the onset.

\[ \gamma = \alpha + j\beta \]

• Gamma can be deduced from measurements of only one line (line) or of two lines (line-line) with different length but the same cross section.
• The first approach neglects the effects of the pad parasitics, which are more noticeable the shorter the line is.
• The propagation constant calculated from the long line only is similar in value from the data obtained from the line-line method.
• This is not so when using the short line only.
• Resonances appear when multiples of \( \lambda/2 \) of the propagating signal equal the length of the line, accentuated by reflections at the pad terminations.
• These appear at lower than expected frequencies using the long line.
• The problem with the line–line method is the impossibility to completely de-embed the effect of the pad parasitics in the $Z_c$ curves.
• The line–line method shifts the frequency of resonance up to a higher value when comparing with the case in which only the longest line is considered.
• This is attributed to the fact that the effective line length is reduced by the subtraction of the shortest to the longest length.
• The fluctuation of the curves around the frequency of resonance impedes the accurate determination of $R$, $L$, $C$ and $G$ per unit length length.
• Assuming that $\gamma$ and $Z_c$ obtained using the line–line method are accurate up to a frequency small enough as to avoid the effect of the first resonance in the $Z_c$ curve, the R, L, C and G components can be determined.
• The graphs show that this frequency has to be lower than 20 GHz.
• Once these values are determined, the data can be analyzed up to 60 GHz, and the CPW can be correctly modeled.
• But, we also have to consider another factor which muddles the correct extraction of the values; this is the coupling of the input and output ports, especially in the case of the short line.
• As in the previous case (integrated inductor) the model considers the dependence of resistance and inductance with frequency.
• An additional component is included to the model to account for the transverse resistance introduced by the PGS, and the coupling between input and output ports.
• This was analyzed for the different gap-width separations.
• Current density substantially increases in the PGS at high frequencies.
• The impedance of the shield is reduced with frequency, forming a path in the direction of propagation.
• This increases current flow through the shield, and promotes an undesired coupling of the CPW’s input and the output ports.
• This effect is especially noticeable in short lines.
$R_{DC} = 5.8 \text{ k}$\Omega/m

$R_{DC} = 5.3 \text{ k}$\Omega/m

$f_{onset} \mid s = 2 \mu$m = 420 MHz

$s = 2 \mu$m

$s = 5 \mu$m

$s = 10 \mu$m
The diagram shows the frequency-dependent capacitance $C_{\alpha x}$ as a function of frequency. The capacitance values are labeled as $C_{\alpha x} = 150 \, \text{pF/m}$ and $C_{\alpha x} = 158 \, \text{pF/m}$. The symbols used are:

- $s = 2 \, \mu\text{m}$
- $s = 5 \, \mu\text{m}$
- $s = 10 \, \mu\text{m}$

The lines represent constant values, while the symbols indicate different spacing values. The frequency range is from 0 to 60 GHz.
symbols: line-line
solid lines: model

- $s = 2 \mu\text{m}, R_g = 13 \text{ m}\Omega/\text{m}$
- $s = 5 \mu\text{m}, R_g = 8.7 \text{ m}\Omega/\text{m}$
- $s = 10 \mu\text{m}, R_g = 12 \text{ m}\Omega/\text{m}$

Frequency (GHz)

$G (\Omega^{-1}/\text{m})$
<table>
<thead>
<tr>
<th>Component</th>
<th>PGS-CPW</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>$s = 2 \mu m$</td>
</tr>
<tr>
<td>$R_{dc}$ (k$\Omega$/m)</td>
<td>5.8</td>
</tr>
<tr>
<td>$L_{\infty}$ ($\mu$H/m)</td>
<td>0.52</td>
</tr>
<tr>
<td>$k_s$ (mH s$^{-1/2}$/m)</td>
<td>8.42</td>
</tr>
<tr>
<td>$k$</td>
<td>0.137</td>
</tr>
<tr>
<td>$\tau$ (ps)</td>
<td>24.4</td>
</tr>
<tr>
<td>$C_{ox}$ (pF/m)</td>
<td>186</td>
</tr>
<tr>
<td>$R_g$ (m$\Omega$/m)</td>
<td>9.83</td>
</tr>
</tbody>
</table>
Results

• The inclusion of \( R_g \) in the proposed model allows the accurate modeling of the behavior of PGS-CPWs at high frequencies.
• The method can be used for other types of lines.
• The challenge of fully de-embedding data using only two lines of different lengths was achieved.
• A systematic and physically based modeling of the undesired coupling of the input and output ports through the shield was developed and demonstrated.
Conclusion
• The field of compact modeling grows in importance day by day, as ever more devices can be fit into an IC.
• Besides active devices, passive ones have to also be modeled in order to guarantee the correct response of the circuit, first at the simulation level, and then in practice.
• Good models also give insight into the physical behavior of the device, circuit or system.
• As technology progresses, the need for more sophisticated ICs arises.
• Thus, compact modeling continues to be a very fertile and promising field of endeavor.
Thank you for your kind attention!